ABSTRACT

A semiconductor element (111) with electrodes (112), and a passive element (113) with electrodes (113a) are embedded in a thermoplastic sheet base (115), which is then subjected to laser beam machining, electron beam machining or ion beam machining to expose electrodes (112 and 113a). Thereafter, a circuit pattern (119) is formed by formation of a thin film or printing of a conductive adhesive. Exposing the electrodes by laser beam machining or the like can be carried out in a short period of time and also by local treatment, thereby reducing damage to the base.

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